

Eine Vielzahl von embedded Lösungen setzt neben Prozessorleistung eine immer wieder ähnliche Menge von Schnittstellen voraus. Zu diesen Prozess-Schnittstellen zählen z.B. Ethernet, USB, SPI, I²C und Grafik. Um das zu gewährleisten wird eine standardisierte Pinbelegung auf den Steckverbindern benötigt. Somit wird die skalierbare Rechenleistung über Controller-Architekturen hinweg realisiert.

Dir	Pegel	Signal	Pin	Pin	Signal	Pegel	Dir
In	VCC	VCC	1A	1B	VCC	VCC	In
In	VCC	VCC	2A	2B	VCC	VCC	In
In	VCC	VCC	3A	3B	VCC	VCC	In
-		GND	4A	4B	GND		-
Out	VCC_LOGIC	VCC_LOGIC	5A	5B	VCC_LOGIC	VCC_LOGIC	Out
-	VCC	Voltage-Select	6A	6B	VSTBY	VSTBY	In
In	VCC	nRESET IN	7A	7B	nRESET_OUT	VCC	Out
-		GND	8A	8B	GND		-
Out	LVDS	LVDS TX0+	9A	9B	LVDS_TX1+	LVDS	Out
Out	LVDS	LVDS TX0-	10	10	LVDS_TX1-	LVDS	Out
Out	LVDS	LVDS TX2+	11	11	LVDS_TX3+	LVDS	Out
Out	LVDS	LVDS TX2-	12	12	LVDS_TX3-	LVDS	Out
-		GND	13	13	GND		-
Out	LVDS	LVDS TXCLK+	14	14	LVDS_CAM_RX+	LVDS	In
Out	LVDS	LVDS TXCLK-	15	15	LVDS_CAM_RX-	LVDS	In
Out	VCC_LOGIC	LVDS_CAM_MCLK	16	16	LVDS_CAM_nLOCK	VCC_LOGIC	Out
	VCC_LOGIC	I2C_CLK	17	17	I2C_DATA	VCC_LOGIC	
-		GND	18	18	GND		-
Out	VCC_LOGIC	ETH_SPEED	19	19	ETH_LINK	VCC_LOGIC	Out
Out	ETH	ETH_TX+	20	20	ETH_RX+	ETH	In
Out	ETH	ETH_TX-	21	21	ETH_RX-	ETH	In
-		GND	22	22	GND		-
Out	VCC_LOGIC	USB_OTG_PWR1	23	23	USB_PWR2	VCC_LOGIC	Out
In	VCC_LOGIC	USB_OTG_OC1	24	24	USB_OC2	VCC_LOGIC	In
-		GND	25	25	GND		-
Bi	VCC_USB	USB_OTG_VBUS1	26	26	nSuspend_to_RAM	VCC_LOGIC	Out
Bi	USB	USB_OTG_D1-	27	27	USB_D2-	USB	Bi
Bi	USB	USB_OTG_D1+	28	28	USB_D2+	USB	Bi
In	USB	USB_OTG_UID1	29	29	nPower_Off	VCC_LOGIC	Out
-		GND	30	30	GND		-
Bi	VCC_LOGIC	SDIO_D0	31	31	SDIO_D1	VCC_LOGIC	Bi
Bi	VCC_LOGIC	SDIO_D2	32	32	SDIO_D3	VCC_LOGIC	Bi
Out	VCC_LOGIC	SDIO_CLK	33	33	SDIO_CMD	VCC_LOGIC	Bi
-		GND	34	34	GND		-
Out	VCC_LOGIC	SPI_CS0	35	35	SPI_CS1	VCC_LOGIC	Out
In	VCC_LOGIC	SPI_RDY	36	36	SPI_MOSI	VCC_LOGIC	Out
Out	VCC_LOGIC	SPI_CLK	37	37	SPI_MISO	VCC_LOGIC	In
-		GND	38	38	GND		-
Out	VCC_LOGIC	UART_TXD	39	39	UART_RXD	VCC_LOGIC	In
In	VCC_LOGIC	UART_RTS	40	40	UART_CTS	VCC_LOGIC	Out
-		GND	41	41	GND		-
Bi	VCC_LOGIC	HDA_SEL/AC97_INT	42	42	AC97/HDA_BIT_CLK	VCC_LOGIC	Bi
Out	VCC_LOGIC	AC97/HDA_SDATA_OUT	43	43	AC97/HDA_SYNC	VCC_LOGIC	Out
In	VCC_LOGIC	AC97/HDA_SDATA_IN	44	44	AC97/HDA_nRESET	VCC_LOGIC	Out
-		GND	45	45	GND		-
Bi	VCC_LOGIC	GPIO0/IRQ/PWM	46	46	SDIO_CD	VCC_LOGIC	In
Bi	VCC_LOGIC	GPIO2/IRQ	47	47	GPIO1/IRQ	VCC_LOGIC	Bi
In		nWKUP	48	48	for internal use only		Bi
-		GND	49	49	GND		-
In	VCC_LOGIC	CONFIG0	50	50	CONFIG1	VCC_LOGIC	In