

Purpose: This Product Change Notification (PCN) is to provide notification to PHYTEC customers of component, process or other relevant engineering changes on a PHYTEC hardware subassembly. Impact, qualification, validation and approval of this change shall be documented on the corresponding Customer-Specific Modification (KSM/KSP) form for the PHYTEC hardware subassembly

Product affected / Release Date	
Notice Date: March 11, 2014	LPN #: LPN-048e_1
Date of Change:	New Product Version:
PHYTEC Subassembly:	phyCARD-i.MX35 with 128MByte Flash
PHYTEC Subassembly Part #: PCA-A-M1 with 128 MByte Flash	
New PHYTEC Part #:	

Engineering Change (Component, Firmware, Process, other)		
Current Part		New Part
IM502	PHYTEC Internal Part #	IM656 H600320
Samsung	Manufacturer	Micron
K9F1G08U0C-SIB0	Manufacturer Part #	MT29F1G08ABADAWP-IT
1Gbit Nand Flash	Description	1Gbit Nand Flash
IM656 H600670	PHYTEC Internal Part #	IM706
Samsung	Manufacturer	Spansion
K9F1G08U0D-SIB0	Manufacturer Part #	S34ML01G100TFI00
1Gbit Nand Flash	Description	1Gbit Nand Flash

Engineering Change Details
Reason for Component Change: EOL at Samsung
Referenced Component Documents: Samsung EOL K9F1G08U0D Notification

Impact of Change

- (1) No Impact in fit, form and function
- (2) **Different Manufacture and Device ID's**

Measures taken by PHYTEC

- (1) Datasheet Check
- (2) Test in climatic chamber with Linux BSP
- (3)

Recommended Measures for Customer

- (1) If you using Linux as Operating System, then NONE
- (2) If you using WinCE, then update your system, because Manufacture and Device ID is checking
- (3) Update image of Advanced Toolkit (ATK-Tool) to PHYTEC_mx35_nand_V4.bin

Technical Differences

Parameter	Original K9F1G08U0	Replacement MT29F1G08ABADAWP-IT	Assess- ment ¹
Device ID	0xEC 0xF1 0x00 0x15 0x40	0x2c 0xF1 0x80 0x95 0x02	1
V _{IL} undershoot	-0,4 V at pulse width 20 ns	n.a.	
Read/Write Cycle Time	Min: 25 ns	Min: 20 ns	2
Open NAND Flash Interface (ONFI) 1.0	no	compliant	

Technical Differences

Parameter	Original K9F1G08U0	Replacement S34ML01G100TFI00	Assess- ment
Device ID	0xEC 0xF1 0x00 0x15 0x40	0x01 0xF1 0x00 0x1D	1
V _{IL} undershoot	-0,4 V at pulse width 20 ns	-2 V at pulse width 20 ns	3, 2
Read/Write Cycle Time	Min: 25 ns	Min: 25 ns	2
Open NAND Flash Interface (ONFI) 1.0	no	compliant	

¹ Assessments:

- 1: Effects are to be expected
- 2: No negative effects are to be expected
- 3: Better than before
- 4: Worse than before

Technical Similarities		
Parameter	Original K9F1G08U0	Replacement MT29F1G08ABADAWP-IT S34ML01G100TFI00
Supply Voltage	VCC: 2.7 V to 3.6 V	
Temperature	-40 °C to 85 °C	
Package Pitch, Form	TSOP T 48-P1220-0.50, 0.5 mm Pitch	

Note:
 Technical differences and similarities in the tables above may not be complete. Please refer to the manufacture datasheets for a complete comparison.

Please contact our support if you need any further information.

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