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 Author: M. Franck  
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1.0	First edition	13.10.2017	M. Franck

# Soldering Instruction for the phyCORE® -i.MX 6UL/ULL

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## 1 Introduction

This Application Note provides a guide for how to solder the phyCORE-i.MX 6UL/ULL modules.

For a good soldering result we recommend to use the same or nearly the same parameters.

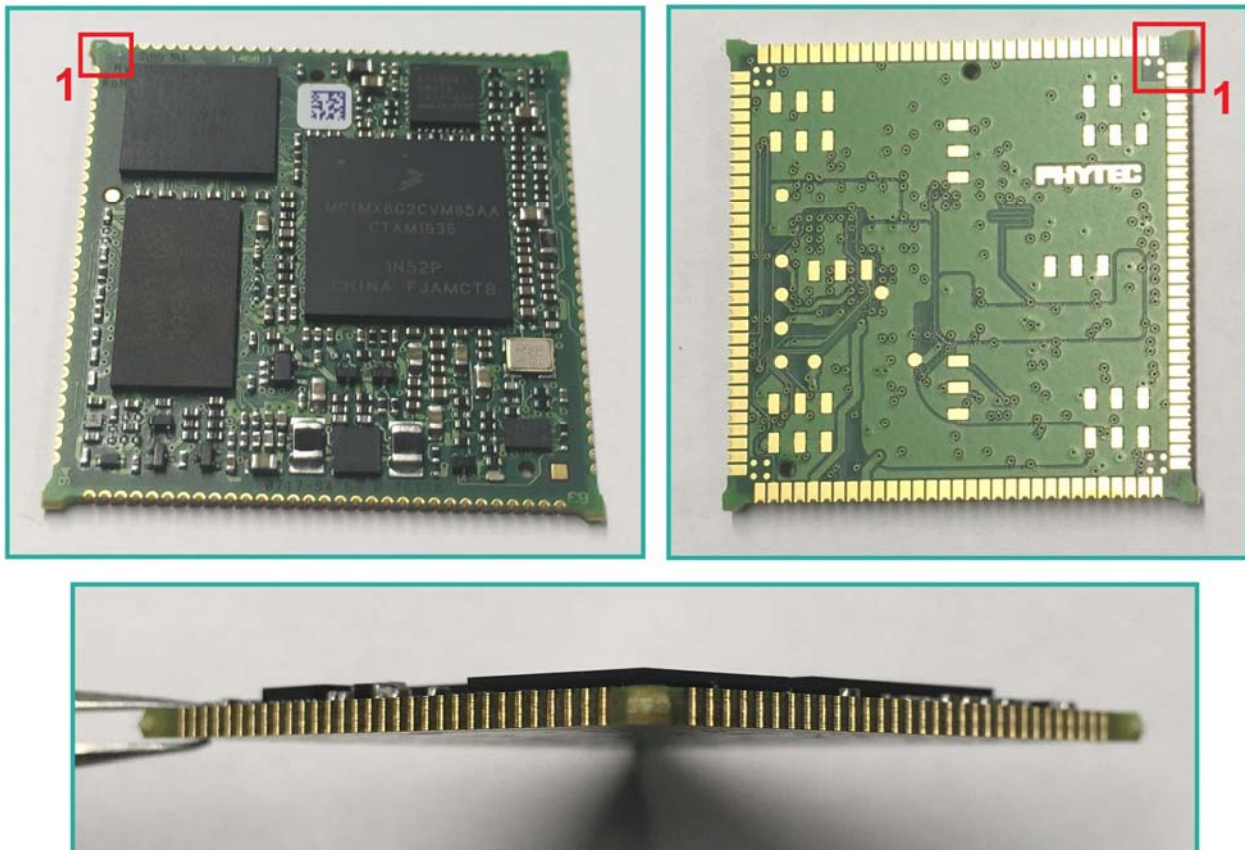


Figure 1: Top, Bottom and Side View



Because of its weight and to reduce the soldering cycles, we recommend populating the SOM on the assembly side of the carrier board with the SOM's assembly side facing upwards.

## 2 Delivery Format

The modules are shipped in 56 mm tapes with a feed of 48 mm (outer reel diameter: 33 cm). 175 pieces are on one reel and 10 slots are empty to allow mounting on a tape feeder.

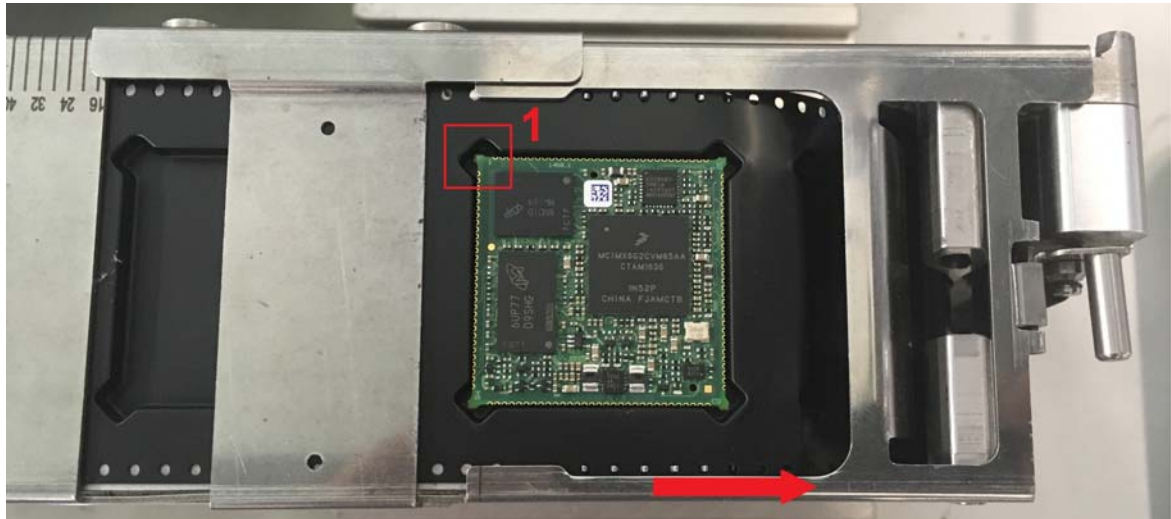
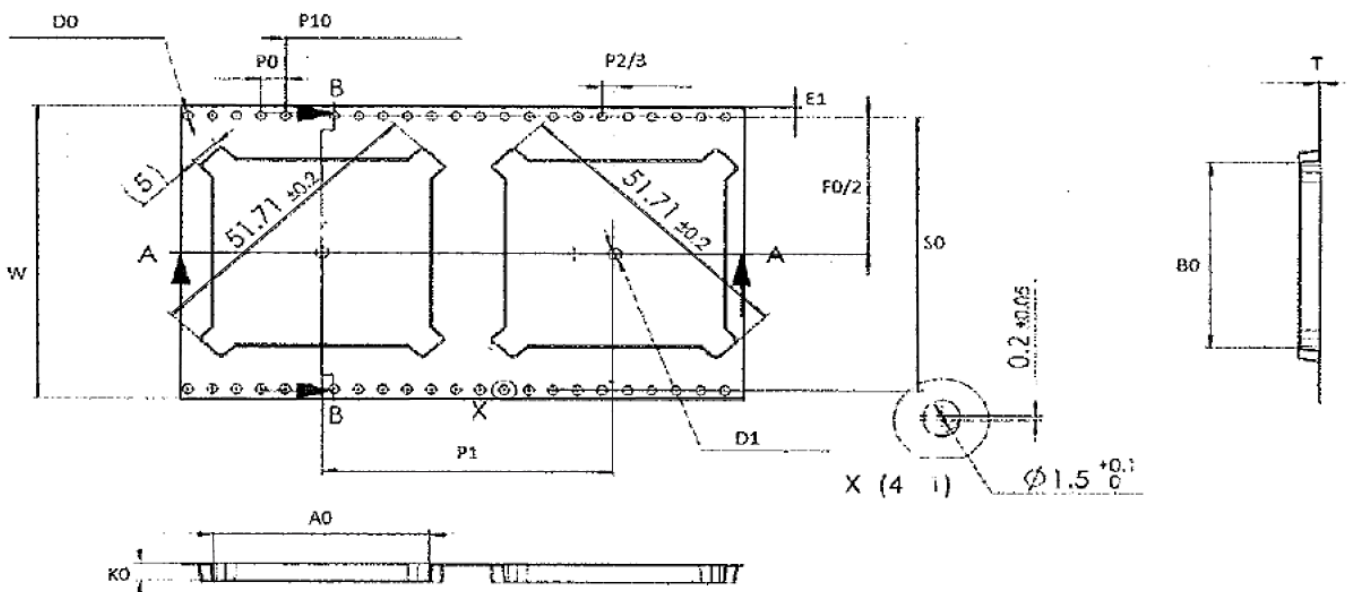


Figure 2: Location of Pin1



- $A_0 = 35.50 \pm 0.20$
- $B_0 = 35.50 \pm 0.20$
- $K_0 = 3.30 \pm 0.20$
- $P_1 = 48.00 \pm 0.10$
- $W = 56.00 \pm 0.30$
- $t = 0.30 \pm 0.05$

All other dimensions and tolerances as per EIA 481 standard.

Figure 3: Tape Dimensions in mm

### 3 Production Settings

The modules are rated MSL 3, so it is required to store them adequately before soldering (see IPC/JEDEC J-STD-033C). I.e. it is recommended to store the modules shrink-wrapped in the original packing or, if the packing is opened, to store them in a humidity reduced storage (rel. humidity < 10%, T < 30°C / 86°F). The shelf life in the sealed packaging amounts 12 months at < 40°C and < 90 % rel. humidity.

#### 3.1 Paste Printing

For the paste printing we recommend at least a 100 µm stencil without pad reduction. We use type 4 paste.

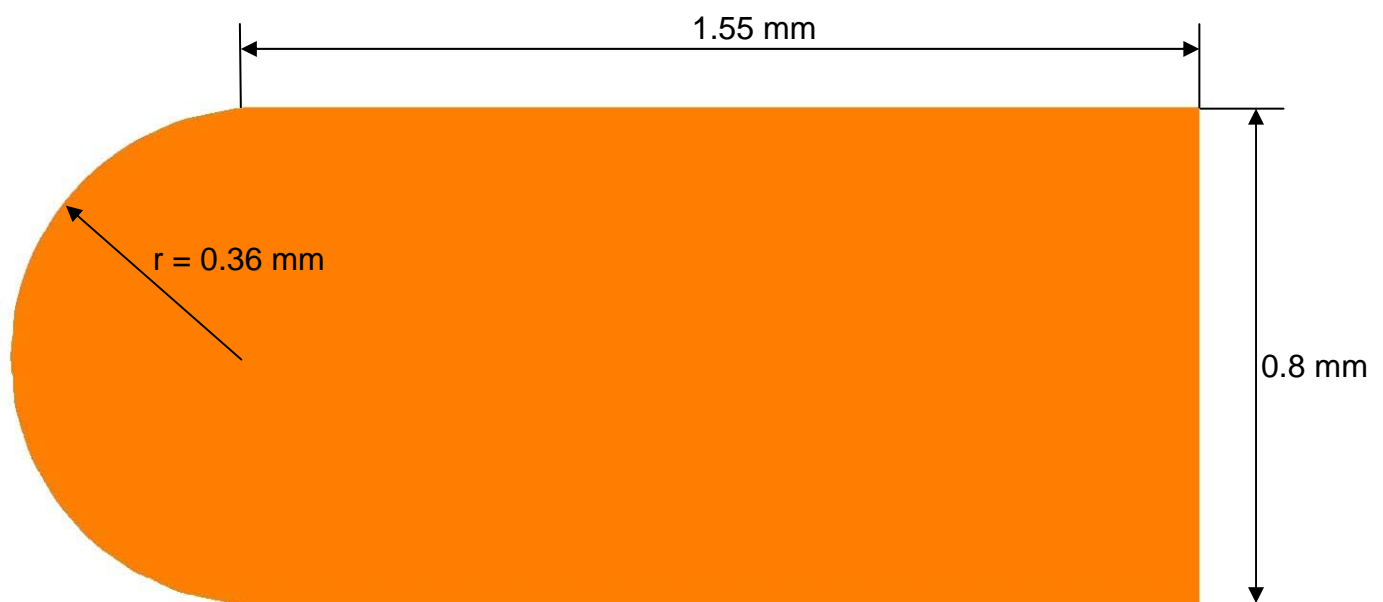


Figure 4: Paste Printing (orange) without Paste Reduction

### 3.2 Placement

For good accuracy the modules have to be placed with a placement machine. Pin 1 is in the corner of the NAND Flash (green marker). With the fix camera of the placement machine we check the fiducials on the bottom of the SOM. Pin 1 is marked with only 3 fiducials instead of 4, as can be seen in [Figure 1](#).

The nozzle has to be able to pick up at least 6 grams, we pick the SOM with a Samsung CP45 TN110 nozzle in the middle of the module and place the module last on the PCB.

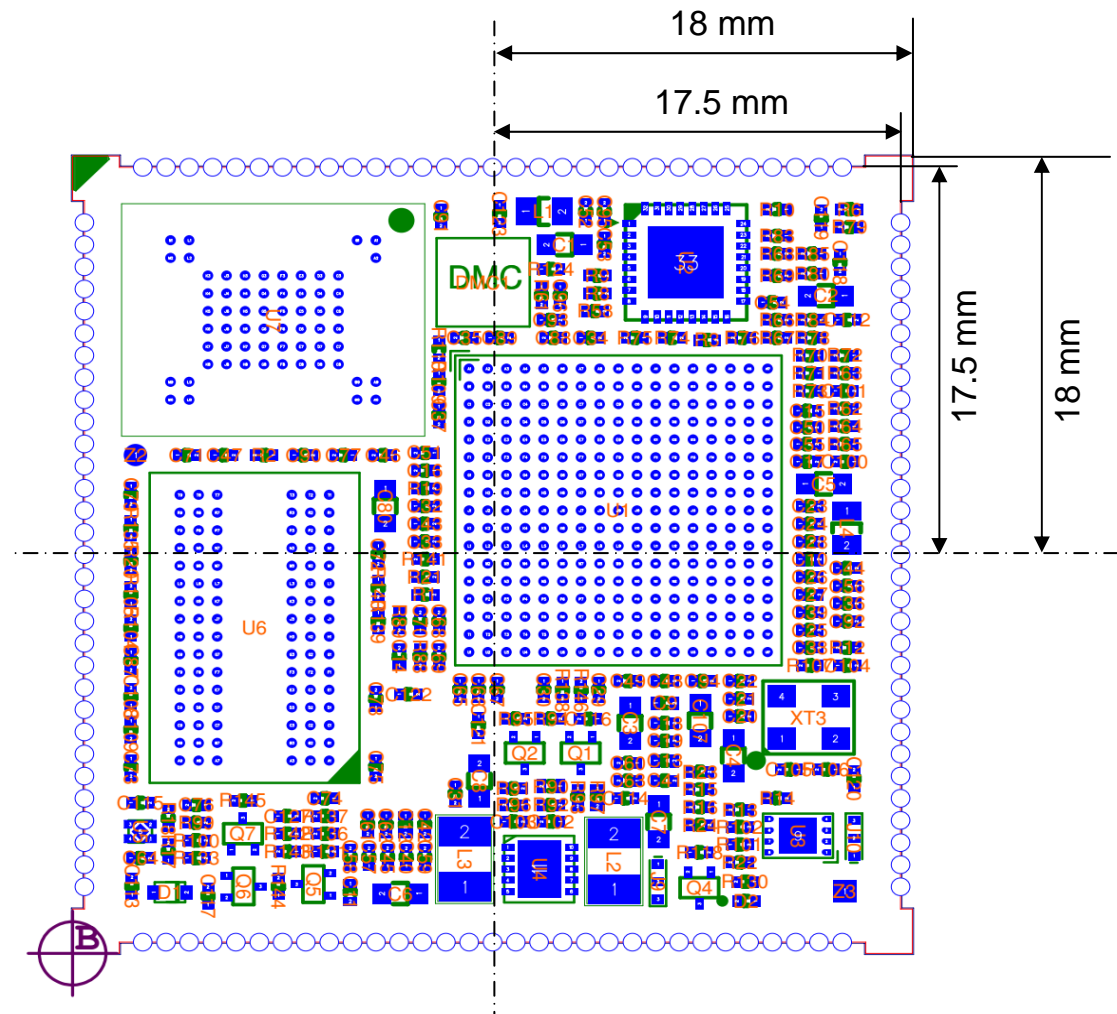


Figure 5: Dimensions of the Module

The link [phyCORE-i.MX 6UL/ULL](#) within the category *Dimensioned Drawing* on the phyCORE-i.MX 6UL/ULL download page (<http://www.phytec.de/produkt/system-on-modules/phycore-imx-6ul-download/>) leads to the layout data. It is available in different file formats. Use of this data allows integrating the phyCORE-i.MX 6UL/ULL SOM as a single component into your design.

### 3.3 Soldering

Our soldering profiles for the vapor phase soldering is based on J-STD-020E (see below). The liquidous time amounts between 60 s and 90 s, and we recommend a maximum of three soldering processes. The peak body temperature of the populated parts is 260°C.

#### Classification Profile (J-STD-020E<sup>1</sup>, Table 5-2, Page 8):

Profile Feature		Pb-Free Assembly
Preheat/Soak Temperature Min (T <sub>smin</sub> ) Temperature Max (T <sub>smax</sub> ) Time (ts) from (T <sub>smin</sub> to T <sub>smax</sub> )		150 °C 200 °C 60-120 seconds
Ramp-up rate (TL to T <sub>p</sub> )		3 °C/second max.
Liquidous temperature (TL) Time (tL) maintained above TL		217 °C 60-150 seconds
Peak package body temperature (T <sub>p</sub> )		For users T <sub>p</sub> must not exceed the Classification temp in Table 4-2. For suppliers T <sub>p</sub> must equal or exceed the Classification temp in Table 4-2.
Time (t <sub>p</sub> )* within 5 °C of the specified classification temperature (T <sub>c</sub> ), see Figure 5-1.		30* seconds
Ramp-down rate (T <sub>p</sub> to TL)		6 °C/second max.
Time 25 °C to peak temperature		8 minutes max.
* Tolerance for peak profile temperature (T <sub>p</sub> ) is defined as a supplier minimum and a user maximum.		

**Note 1:** All temperatures refer to the center of the package, measured on the package body surface that is facing up during assembly reflow (e.g., live-bug). If parts are reflowed in other than the normal live bug assembly reflow orientation (i.e., dead-bug), T<sub>p</sub> shall be within ±2°C of the live bug T<sub>p</sub> and still meet the T<sub>c</sub> requirements, otherwise, the profile shall be adjusted to achieve the latter. To accurately measure actual peak package body temperatures, refer to JEP140 for recommended thermocouple use.

**Note 2:** Reflow profiles in this document are for classification/preconditioning and are not meant to specify board assembly profiles. Actual board assembly profiles should be developed based on specific process needs and board designs and should not exceed the parameters in this table.

For example, if T<sub>c</sub> is 260 °C and time T<sub>p</sub> is 30 seconds, this means the following for the supplier and the user:

- For a supplier: The peak temperature must be at least 260 °C. The time above 255 °C must be at least 30 seconds.
- For a user: The peak temperature must not exceed 260 °C. The time above 255 °C must not exceed 30 seconds.

**Note 3:** All components in the test load shall meet the classification profile requirements.

**Note 4:** SMD packages classified to a given moisture sensitivity level by using Procedures or Criteria defined within any previous version of J-STD-020, JESD22-A112 (rescinded), IPC-SM-786 (rescinded) do not need to be reclassified to the current revision unless a change in classification level or a higher peak classification temperature is desired.

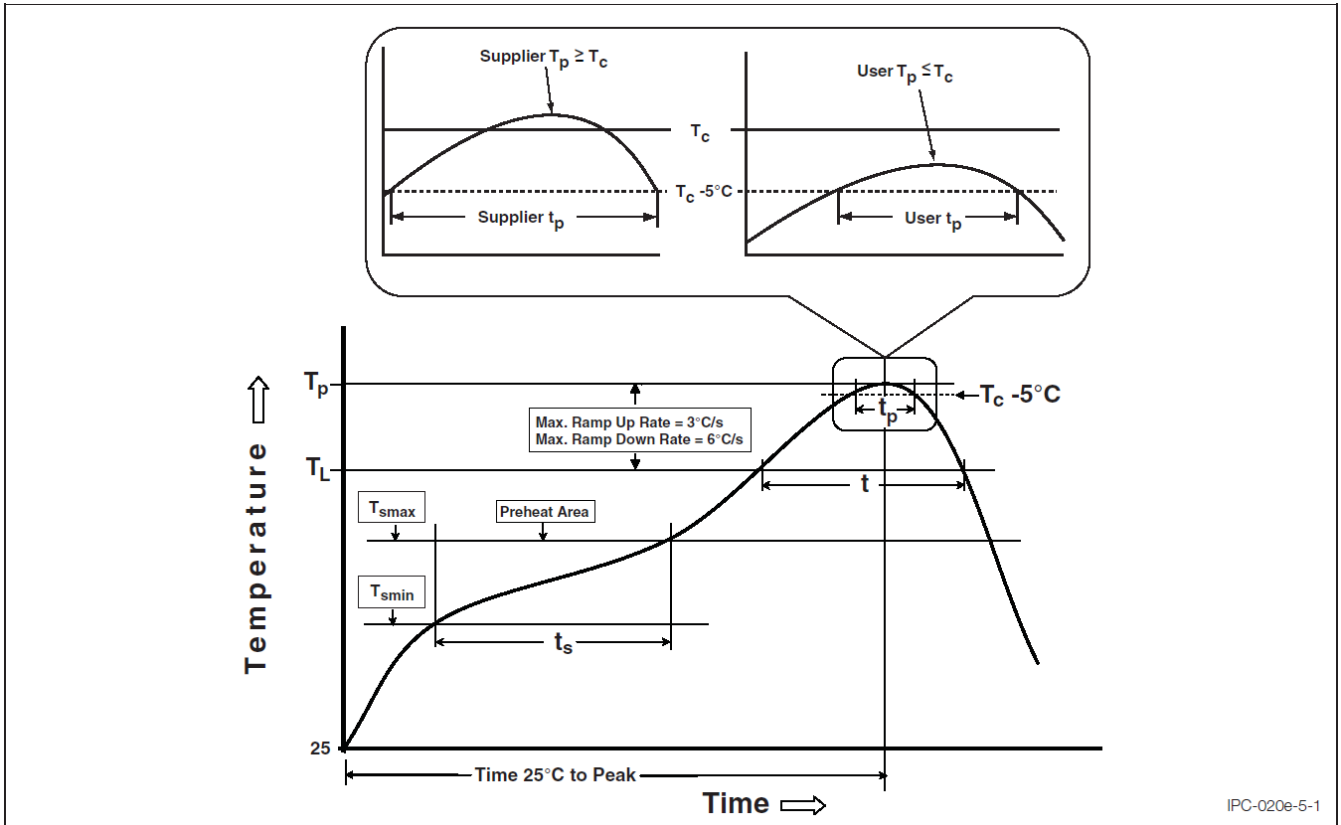


Figure 6: Classification Profile (J-STD-020E<sup>1</sup>, Figure 5-1, Page 8)

Please contact our technical support, if you need additional information, or if you have any questions.

<b>Europe (except France):</b>	<b>France:</b>
▪ +49 6131 9221-31	▪ +33 2 43 29 22 33
▪ <a href="mailto:support@phytec.de">support@phytec.de</a>	▪ <a href="mailto:support@phytec.fr">support@phytec.fr</a>
<b>North America:</b>	<b>India:</b>
▪ +1 206 780-9047	▪ +91-80-4086 7047/50
▪ <a href="mailto:support@phytec.com">support@phytec.com</a>	▪ <a href="mailto:support@phytec.in">support@phytec.in</a>
<b>China:</b>	
▪ +86-755-6180-2110	
▪ <a href="mailto:support@phytec.cn">support@phytec.cn</a>	

<sup>1</sup>: Revision December 2014