

Purpose: This Product Change Notification (PCN) is to provide notification to PHYTEC customers of component, process or other relevant engineering changes on a PHYTEC hardware subassembly. Impact, qualification, validation and approval of this change shall be documented on the corresponding Customer-Specific Modification (KSM/KSP) form for the PHYTEC hardware subassembly

Per JEDEC Standard JESD46-D Section 3.2.3; lack of acknowledgment of this PCN within 30 days constitutes acceptance of change

Type of Change		
Notice Date: 2017. 06.20 <yyyymm.dd>		LPN #: LPN-205e_2
<input checked="" type="checkbox"/> Major Change <input type="checkbox"/> Minor Change		
Description of Change: Due to EOL announcement from Innolux for the 7.0" TFT panel, with 800RGB x 480, EDT has decided to use new Innolux panel. The new display of Glyn receive the panel upgrade, optimised PCB and the latest PCAP generation (product code: M12) Adaptations to the PHYTEC hardware and software were necessary for the new display, because the Pixel Clock direction and the Touchcontroller was changed.		
Referenced Documents: Glyn PDN about 7" display of February 2016		
Type of Change: <input type="checkbox"/> Lifecycle change to <input checked="" type="checkbox"/> Component change <input type="checkbox"/> Software update <input type="checkbox"/> Other	Component Change <input type="checkbox"/> Controller <input type="checkbox"/> PCB <input type="checkbox"/> RAM <input type="checkbox"/> FLASH <input checked="" type="checkbox"/> Other Display	Software Update necessary <input checked="" type="checkbox"/> Linux <input checked="" type="checkbox"/> Android <input checked="" type="checkbox"/> Windows <input checked="" type="checkbox"/> Other
Product Affected		
Affected PHYTEC Productgroup: phyFlex-i.MX6 Kit, phyBOARD-Mira, phyBOARD-Wega, phyCard-AM335, phyCore-AM335		
Affected PHYTEC Productgroup Part: KPFL-A-02, PB-01501, PB-00802, KPCA-A-XS1, KPCM-051		
Anticipated impact on Form, Fit, Function, EMI, Quality or Reliability (positive / negative):		
(1) no impact in form or fit (2) impact in function		
Possible Measures		
<input checked="" type="checkbox"/> Change to new PHYTEC product revision with replacement part <input type="checkbox"/> Change to different option of product <input type="checkbox"/> Change to different PHYTEC Product <input checked="" type="checkbox"/> Interims stock \ final stock		
Schedule		
Last Time Buy (current product version): . . <yyyymm.dd> (Last date to set an order for the current product version) ORDERS ARE NON-CANCELABLE AND NON-RETURNABLE.		
Samples of new PHYTEC product revision available: Q3 / 2016		
Mass production of new PHYTEC product revision: Q2 / 2017		

Product Affected	
Affected Product Number	Replacement Product Number
KPFL-A-02-LIN-D-I.A1	KPFL-A-02-LIN-D-I.A2
KPFL-A-02-LIN-D.A1	KPFL-A-02-LIN-D.A2
KPCM-051-LIN-D	KPCM-051-LIN-D.A1
	KPCM-060-LIN-D
PB-01501-007.A0	PB-01501-007.A1
PB-01501-005.A1	PB-01501-005.A2
PB-00802-010.A0	PB-00802-010.A1

Engineering Change (Component, Firmware, Process, other)		
Current Part		New Part
AC104	PHYTEC Internal Part #	AC158
EDT Elektronik GmbH	Manufacturer	EDT Elektronik GmbH
ETM0700G0DH6	Manufacturer Part #	ETM0700G0EDH6
7" 800x480, TFT,capTouch	Description	7" 800x480, TFT,capTouch

Technical Parameter			
Parameter	Original ETM0700G0DH6	Replacement ETM0700G0EDH6	Assess- ment ¹
Colour rendition (NTSC colour gamut)	45 %	72 %	1
pixel-clock inverter	yes	no	1
Touch-Sensor	version M06	version M12	1
Touch Controller	FocalTech FT5x0	HyCon HY461x	1

¹ Assessments:
 1: Effects are to be expected
 2: No negative effects are to be expected

PHYTEC Qualification	
The new product(s) were qualified according to our Company qualification procedure and best practices.	
<input type="checkbox"/> PCB redesign was necessary, because	<input checked="" type="checkbox"/> Software Adaption was necessary, because Linux: to support the new touch controller and hardware Windows: to support the new touch controller and hardware Android: to support the new touch controller and hardware
<input type="checkbox"/> Software tests were conducted with BSP for testing: Test program:	

Recommended Measures for Customer
<input checked="" type="checkbox"/> Software update or patch <input checked="" type="checkbox"/> Linux BSP: is in progress <input type="checkbox"/> backward compatible phyFlex-i.MX6 Kit: PD15.3.0 or newer phyBOARD-Mira: PD15.3.0 or newer phyCORE-AM335 Kit: PD16.2.0 or newer phyBOARD-Wega: plan for PD17.1.0, because QtDemo Kernel Problems in PD16.2.0 <input type="checkbox"/> Windows BSP: is in progress <input type="checkbox"/> backward compatible Link: <input type="checkbox"/> Android BSP: <input type="checkbox"/> backward compatible Link:
<input type="checkbox"/> Update Programming Tool
<input checked="" type="checkbox"/> Fit integration test with your system and case. Phytect recommends that customers take this opportunity to review these changes against current application notes, system design considerations and customer environment conditions to assess impact (if any) to their application.

Note:
 Technical differences and similarities in the tables above may not be complete. Please refer to the manufacture datasheets for a complete comparison.

Patch for PD16.2.0 phyBOARD-Wega to fix QtDemo Kernel problem

Signed-off-by: Daniel Schultz <d.schultz@phytec.de>

```

.../libgles/ti-sgx-ddk-um_1.14.3699939.bb      | 3 +-
recipes-kernel/linux/linux-ti_4.4.19-phy2.bb | 51 -----
recipes-kernel/linux/linux-ti_4.4.52-phy2.bb | 52 ++++++
3 files changed, 54 insertions(+), 52 deletions(-)
delete mode 100644 recipes-kernel/linux/linux-ti_4.4.19-phy2.bb
create mode 100644 recipes-kernel/linux/linux-ti_4.4.52-phy2.bb

```

```
diff --git a/recipes-graphics/libgles/ti-sgx-ddk-um_1.14.3699939.bb b/recipes-graphics/libgles/ti-sgx-ddk-um_1.14.3699939.bb
```

```
index 27450b7..2459d8b 100644
```

```
--- a/recipes-graphics/libgles/ti-sgx-ddk-um_1.14.3699939.bb
```

```
+++ b/recipes-graphics/libgles/ti-sgx-ddk-um_1.14.3699939.bb
```

```
@@ -12,7 +12,8 @@ SRC_URI = "\
file://0001-rc.pvr-add-proper-error-return-code.patch \
file://0002-rc.pvr-don-t-load-the-bc_example-module.patch \
"
```

```
-SRCREV = "fccafce7d0a023e36c064ccbaa7d33e89252ea00"
```

```
+#SRCREV = "fccafce7d0a023e36c064ccbaa7d33e89252ea00"
```

```
+SRCREV = "d184140aa5c17e13e1bf21151f1a7bc068bdf8bf"
```

```
# There's only hardfp version available
```

```
inherit tune_features_check
```

```
diff --git a/recipes-kernel/linux/linux-ti_4.4.19-phy2.bb b/recipes-kernel/linux/linux-ti_4.4.19-phy2.bb
```

```
deleted file mode 100644
```

```
index 5fe311b..0000000
```

```
--- a/recipes-kernel/linux/linux-ti_4.4.19-phy2.bb
```

```
+++ /dev/null
```

```
@@ -1,51 +0,0 @@
```

```
## Copyright (C) 2014-2015 PHYTEC Messtechnik GmbH,
```

```
## Author: Stefan Mueller-Klieser <s.mueller-klieser@phytec.de>
```

```
## DESCRIPTION = "Linux Kernel provided and supported by PHYTEC based on TIs \
```

```
## Kernel for AM335x Family Boards. It includes support for \
```

```
## many IPs such as GPU, VPU and IPU."
```

```
##
```

```
##-inherit phygittag
```

```
##-inherit buildinfo
```

```
##-include linux-common.inc
```

```
##
```

```
##-GIT_URL = "git://git.phytec.de/${PN}"
```

```
##-SRC_URI = "${GIT_URL};branch=${BRANCH}"
```

```
##-SRC_URI_append = "\
```

```
##- ${@bb.utils.contains('MACHINE_FEATURES', 'sgx', 'file://preempt_voluntary.cfg', '', d)} \
```

```
##- ${@bb.utils.contains('MACHINE_FEATURES', 'suspend', 'file://am335x-cm3.cfg', '', d)} \
```

```
##- ${@bb.utils.contains('MACHINE_FEATURES', '3g', 'file://3g.cfg', '', d)} \
```

```
##- ${@base_conditional('DEBUG_BUILD', '1', 'file://debugging.cfg', '', d)} \
```

```
##"
```

```
##
```

```
##-S = "${WORKDIR}/git"
```

```
##
```

```
##-PR = "${INC_PR}.0"
```

```
##
```

```
##-# NOTE: Keep version in filename in sync with commit id!
```

```
##-SRCREV = "ab663b532f6d8172ed0d298efdfc988f9d422b0d"
```

```
##
```

```
##-RDEPENDS_kernel-modules_ti33x = "\
```

```
##- ${@bb.utils.contains('MACHINE_FEATURES', 'suspend', 'amx3-cm3', '', d)} \
```

```
##- ${@bb.utils.contains('MACHINE_FEATURES', 'sgx', 'ti-sgx-ddk-km', '', d)} \
```

```
##- cryptodev-module \
```

```

-"
-
-COMPATIBLE_MACHINE = "beagleboneblack-1"
-COMPATIBLE_MACHINE .= "|phyboard-wega-am335x-1"
-COMPATIBLE_MACHINE .= "|phyboard-wega-am335x-2"
-COMPATIBLE_MACHINE .= "|phyboard-wega-am335x-3"
-COMPATIBLE_MACHINE .= "|phyboard-wega-am335x-4"
-COMPATIBLE_MACHINE .= "|phyboard-wega-r2-am335x-1"
-COMPATIBLE_MACHINE .= "|phycore-am335x-1"
-COMPATIBLE_MACHINE .= "|phycore-am335x-2"
-COMPATIBLE_MACHINE .= "|phycore-am335x-3"
-COMPATIBLE_MACHINE .= "|phycore-am335x-4"
-COMPATIBLE_MACHINE .= "|phycore-am335x-5"
-COMPATIBLE_MACHINE .= "|phycore-am335x-6"
-COMPATIBLE_MACHINE .= "|phycore-am335x-7"
-COMPATIBLE_MACHINE .= "|phyboard-regor-am335x-1"
-COMPATIBLE_MACHINE .= "|phycore-r2-am335x-1"
-COMPATIBLE_MACHINE .= "|phycore-r2-am335x-2"
-COMPATIBLE_MACHINE .= "|phycore-r2-am335x-3"
-COMPATIBLE_MACHINE .= "|phycore-r2-am335x-4"
-COMPATIBLE_MACHINE .= "|phycore-r2-am335x-5"
diff --git a/recipes-kernel/linux/linux-ti_4.4.52-phy2.bb b/recipes-kernel/linux/linux-ti_4.4.52-phy2.bb
new file mode 100644
index 0000000..e0dac30
--- /dev/null
+++ b/recipes-kernel/linux/linux-ti_4.4.52-phy2.bb
@@ -0,0 +1,52 @@
+# Copyright (C) 2014-2015 PHYTEC Messtechnik GmbH,
+# Author: Stefan Mueller-Klieser <s.mueller-klieser@phytec.de>
+DESCRIPTION = "Linux Kernel provided and supported by PHYTEC based on TIs \
+      Kernel for AM335x Family Boards. It includes support for \
+      many IPs such as GPU, VPU and IPU."
+
+inherit phygittag
+inherit buildinfo
+include linux-common.inc
+
+GIT_URL = "git://git.phytec.de/${PN}"
+SRC_URI = "${GIT_URL};branch=${BRANCH}"
+SRC_URI_append = "\
+  ${@bb.utils.contains('MACHINE_FEATURES', 'sgx', 'file://preempt_voluntary.cfg', '', d)} \
+  ${@bb.utils.contains('MACHINE_FEATURES', 'suspend', 'file://am335x-cm3.cfg', '', d)} \
+  ${@bb.utils.contains('MACHINE_FEATURES', '3g', 'file://3g.cfg', '', d)} \
+  ${@base_conditional('DEBUG_BUILD', '1', 'file://debugging.cfg', '', d)} \
+"
+
+S = "${WORKDIR}/git"
+
+PR = "${INC_PR}.0"
+
+# NOTE: Keep version in filename in sync with commit id!
+#SRCREV = "ab663b532f6d8172ed0d298efdfc988f9d422b0d"
+SRCREV = "90038b6d8506b5daf5fef663a8cfa03de851414"
+
+RDEPENDS_kernel-modules_ti33x = "\
+  ${@bb.utils.contains('MACHINE_FEATURES', 'suspend', 'amx3-cm3', '', d)} \
+  ${@bb.utils.contains('MACHINE_FEATURES', 'sgx', 'ti-sgx-ddk-km', '', d)} \
+  cryptodev-module \
+"
+
+COMPATIBLE_MACHINE = "beagleboneblack-1"

```

+COMPATIBLE_MACHINE .= "|phyboard-wega-am335x-1"
+COMPATIBLE_MACHINE .= "|phyboard-wega-am335x-2"
+COMPATIBLE_MACHINE .= "|phyboard-wega-am335x-3"
+COMPATIBLE_MACHINE .= "|phyboard-wega-am335x-4"
+COMPATIBLE_MACHINE .= "|phyboard-wega-r2-am335x-1"
+COMPATIBLE_MACHINE .= "|phycore-am335x-1"
+COMPATIBLE_MACHINE .= "|phycore-am335x-2"
+COMPATIBLE_MACHINE .= "|phycore-am335x-3"
+COMPATIBLE_MACHINE .= "|phycore-am335x-4"
+COMPATIBLE_MACHINE .= "|phycore-am335x-5"
+COMPATIBLE_MACHINE .= "|phycore-am335x-6"
+COMPATIBLE_MACHINE .= "|phycore-am335x-7"
+COMPATIBLE_MACHINE .= "|phyboard-regor-am335x-1"
+COMPATIBLE_MACHINE .= "|phycore-r2-am335x-1"
+COMPATIBLE_MACHINE .= "|phycore-r2-am335x-2"
+COMPATIBLE_MACHINE .= "|phycore-r2-am335x-3"
+COMPATIBLE_MACHINE .= "|phycore-r2-am335x-4"
+COMPATIBLE_MACHINE .= "|phycore-r2-am335x-5"

Please contact our order team to ask for an interims or final stock for components or PHYTEC products

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Revision History of the Document

- _1: Initial Document
- _2: Update Information for Software and Productversion

Documentversion: 2016-rev09