PEB-A-002
(USB to Ethernet)

Hardware Manual

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Conventions, Abbreviations and Acronyms

This hardware manual describes the PEB-A-002 PHYTEC Extension Board. The manual specifies the extension boards design and function. Precise specifications for the SMCS LAN9500i Ethernet controller can be found in the Data Sheet/User's Manual.

Conventions

The conventions used in this manual are as follows:

- Signals that are preceded by a "n", "/", or “#”character (e.g.: nRD, /RD, or #RD), or that have a dash on top of the signal name (e.g.: $\overline{RD}$) are designated as active low signals. That is, their active state is when they are driven low, or are driving low.
- A "0" indicates a logic zero or low-level signal, while a "1" represents a logic one or high-level signal.
- Tables which describe jumper settings show the default position in **bold, blue text**.
- Text in *blue italic* indicates a hyperlink within, or external to the document. Click these links to quickly jump to the applicable URL, part, chapter, table, or figure.
- References made to the expansion connectors always refer to pin header connector X8 and X9 on the phyCARD Carrier Board.

Abbreviations and Acronyms

Many acronyms and abbreviations are used throughout this manual. Use the table below to navigate unfamiliar terms used in this document.

Abbreviation	Definition
BSP	Board Support Package (Software delivered with the Development Kit including an operating system (Windows, or Linux) preinstalled on the module and Development Tools).
CB	Carrier Board; used in reference to the phyBASE Development Kit Carrier Board.

Abbreviation	Definition
EMI	Electromagnetic Interference.
GPI	General purpose input.
GPIO	General purpose input and output.
GPO	General purpose output.
J	Solder jumper; these types of jumpers require solder equipment to remove and place.
JP	Solderless jumper; these types of jumpers can be removed and placed by hand with no special tools.
PCB	Printed circuit board.
PEB	PHYTEC Extension Board
PDI	PHYTEC Display Interface
SBC	Single Board Computer; used in reference to the PCA-A-xx /phyCARD-A-xx Single Board Computer
SMT	Surface mount technology.
Sx	User button Sx (e.g. S1, S2, etc.) used in reference to the available user buttons, or DIP-Switches
Sx_y	Switch y of DIP-Switch Sx; used in reference to the DIP-Switches on the display adapter or Carrier Board.

Table 1: Abbreviations and Acronyms used in this Manual

Note:
The BSP delivered with the phyCARDs usually includes drivers and/or software for also controlling the PHYTEC Extension Boards. Therefore programming close to hardware at register level is not necessary in most cases. For this reason, this manual contains no information relevant for software development. Please refer to the Quickstart Manual "OSELAS.BSP()" for phyCARDs and the Ethernet controller's Datasheet if such information is needed.

Preface

As a member of PHYTEC's new phyCARD product family the PEB-A-002 is one of a series of PHYTEC Extension Boards that provide additional functions and interfaces to the standard phyCARD Carrier Board.

PHYTEC's new phyCARD Rapid Development Kit family consists of a series of extremely compact embedded control engines featuring various processing performance classes while using the newly developed X-Arc embedded bus standard. The standardized connector footprint and pin assignment of the X-Arc bus makes this new SBC generation extremely scalable and flexible. This also allows to use the same carrier board to create different applications depending on the required processing power. PHYTEC Extensions Boards (PEBs) facilitate adding even more functions and interfaces. With this new SBC concept it is possible to design entire embedded product families around vastly different processor performances while optimizing overall system cost. In addition, future advances in processor technology are already considered with this new embedded bus standard making product upgrades very easy. Another major advantage is the forgone risk of potential system hardware redesign steps caused by processor or other critical component discontinuation. Just use one of PHYTEC's other phyCARD SBCs thereby ensuring an extended product life cycle of your embedded application.

PHYTEC supports a variety of PEBs in two ways:

(1) as add-ons for Rapid Development Kits which serve as a reference and evaluation platform

(2) as insert-ready, fully functional OEM extension board, which can be embedded directly into the user’s peripheral hardware design.

Implementation of an OEM-able subassembly as the "core" of a specific function, or interface allows you to focus on the development of customer specific circuitry without expending resources to "re-invent" standard functions and interface circuitry. Furthermore, much

of the value of the PHYTEC Extension Boards lies in its layout and test.

Production-ready Board Support Packages (BSPs) and Design Services for our hardware will further reduce your development time and risk and allow you to focus on your product expertise. Take advantage of PHYTEC products to shorten time-to-market, reduce development costs, and avoid substantial design issues and risks. With this new innovative full system solution you will be able to bring your new ideas to market in the most timely and cost-efficient manner.

For more information go to:

http://www.phytec.com/services/

Ordering Information

The part numbering of the PHYTEC Extension Boards has the following structure:

PEB-A-*xxx*

Generation

A = First generation

Model number[1]

001 = SPI to CAN
002 = USB to Ethernet
003 = I^2C, or SPI to GPIO

In order to receive product specific information on changes and updates in the best way also in the future, we recommend to register at

http://www.phytec.de/de/support/registrierung.html

You can also get technical support and additional information concerning your product.

[1] : Please also visit our website www.phytec.de for information on additional PEBs

The support section of our web site provides product specific information, such as errata sheets, application notes, FAQs, etc.

http://www.phytec.de/de/support/faq

Declaration of Electro Magnetic Conformity of the PHYTEC PEB-A-002

PHYTEC Single Board Computers (henceforth products) are designed for installation in electrical appliances or as dedicated Evaluation Boards (i.e.: for use as a test and prototype platform for hardware/software development) in laboratory environments.

Caution:
PHYTEC products lacking protective enclosures are subject to damage by ESD and, hence, may only be unpacked, handled or operated in environments in which sufficient precautionary measures have been taken in respect to ESD-dangers. It is also necessary that only appropriately trained personnel (such as electricians, technicians and engineers) handle and/or operate these products. Moreover, PHYTEC products should not be operated without protection circuitry if connections to the product's pin header rows are longer than 3 m.

PHYTEC products fulfill the norms of the European Union's Directive for Electro Magnetic Conformity only in accordance to the descriptions and rules of usage indicated in this hardware manual (particularly in respect to the pin header row connectors, power connector and serial interface to a host-PC).

Implementation of PHYTEC products into target devices, as well as user modifications and extensions of PHYTEC products, is subject to renewed establishment of conformity to, and certification of, Electro Magnetic Directives. Users should ensure conformance following any modifications to the products as well as implementation of the products into target systems.
*

1 Introduction

The PHYTEC Extension Board PEB-A-002 belongs to a series of add-on modules for PHYTEC's phyCARD Single Board Computer module family. These add-on modules allow easy development of complex applications with off-the-shelf components.

PHYTEC Extension Boards PEB-A-xxx are compatible to the two, identical Extension Connectors X8 and X9 on the phyCARD Carrier Board PBA-A-01. Thus they are interchangeable and the same phyCARD / phyBASE combination might serve for different applications just by changing the extension board and the software.

Figure 1 depicts the modular concept of the phyCARD Development Kit family and the usage of the PHYTEC Extension Boards.

All add-on modules are supported within the BSPs[1] available for the different phyCARD SBCs.

The PEB-A-002 is a subminiature (92 x 68 mm) insert-ready interface card populated with the SMCS LAN9500i Ethernet controller. If mounted on the carrier board (phyBASE) it connects to any phyCARD populating the phyBASE via USB interface. It provides a second Ethernet interface in addition to the interface already available on the phyBASE.

Precise specifications for the components populating the board can be found in the applicable *User's Manuals* or *Data Sheets.*

1: To ensure that the PEB of your choice is supported by the BSP use only the latest BSP, or check on the PHYTEC website from which version on the driver is implemented.

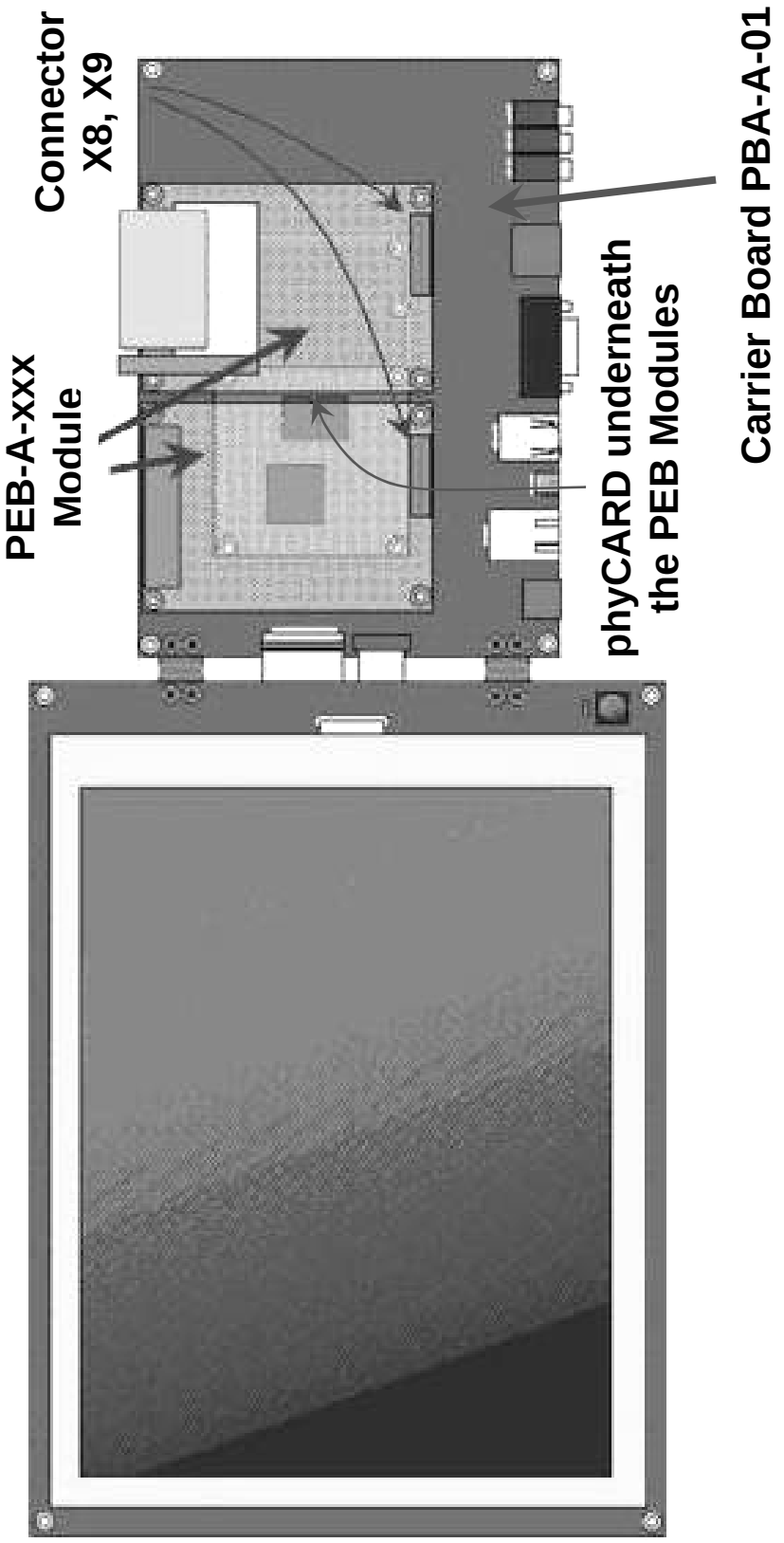


Figure 1: Modular Concept of the phyCARD Development Kit family

The PEB-A-002 offers the following features:

- Subminiature Extension Board (92 x 68 mm) achieved through modern SMD technology
- Compatible to the phyCARD Development Kit Carrier Board (phyBASE) PBA-A-001
- Improved interference safety achieved through multi-layer PCB technology and dedicated Ground pins
- Single supply voltage of 3.3V. Core voltage for Ethernet controller generated on board
- Expansion connector for phyBASE connectivity via USB interface
- Standard 8P8C (RJ45) modular jack
- SMSC LAN9500i Ethernet controller designed to connect to 10Base-T and 100Base-TX networks. It supports Auto HDX/FDX with HP Auto MDI/MDI-X[1]
- Status LEDs to monitor the status of the supply voltage and the Ethernet connectivity
- 1-Wire EEPROM for internal use only
- Mounting wholes to screw the PEB to the phyBASE

1.1 Block Diagram

Figure 2: Block Diagram PEB-A-002

[1]: a complete list of the Ethernet controllers features can be found in the corresponding Datasheet

1.2 View of the PEB-A-002

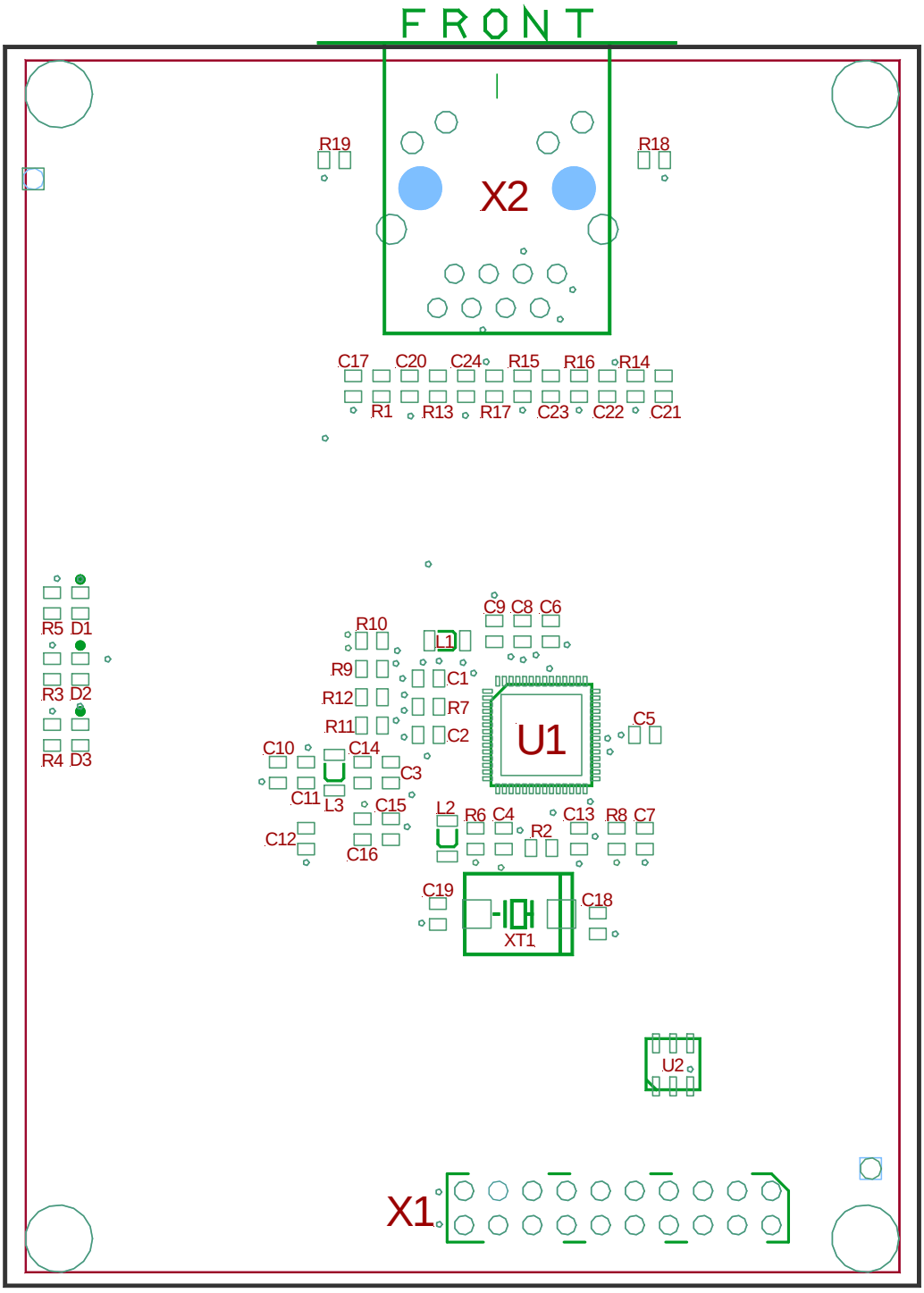


Figure 3: View of the PEB-A-002

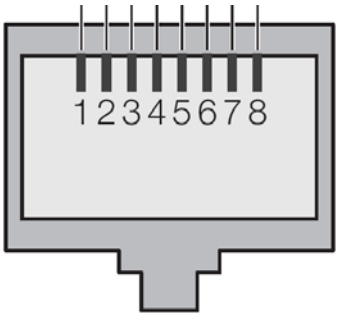
2 Pin Description

Please note that all module connections are not to exceed their expressed maximum voltage or current. Maximum signal input values are indicated in the corresponding controller manuals/data sheets. As damage from improper connections varies according to use and application, it is the user's responsibility to take appropriate safety measures to ensure that the module connections are protected from overloading through connected peripherals.

As shown in *Figure 3* a 2 x 10 connector socket (X1) at the underside of the board connects the PEB-A-002 to the carrier board. The Ethernet interface is available at the RJ45-Jack (X2) on the upper side.

2.1 Pinout of the Ethernet Interface (X2)

The Ethernet interface of the phyCARD is accessible at an RJ45 connector (X2). The LEDs for LINK (green) and SPEED (yellow) indication are integrated in the connector.



Pin 1: TX+
Pin 2: TX-
Pin 3: RX+
Pin 6: RX-

Figure 4: Pin Assignment of the RJ45-Jack X2

2.2 Pinout of the Expansion Connector (X1)

Table 2 provides an overview of the pinout of the expansion connector at X1. The table lists only signals used on the PEB-A-002. Several more signals are available at the expansion connectors X8 and X9 on the phyBASE. Please refer to the hardware manual of your phyCARD for a complete pinout.

Pin #	Signal Name	Description
1	N/C	not connected
2	N/C	not connected
3	VCC3V3	3,3V power supply
4	VCC3V3	3,3V power supply
5	GND	Ground
6	GND	Ground
7	N/C	not connected
8	N/C	not connected
9	PHYWIRE	Hardware Introspection Interface. **For internal use only**
10	GND	Ground
11	N/C	not connected
12	N/C	not connected
13	N/C	not connected
14	N/C	not connected
15	N/C	not connected
16	N/C	not connected
17	GND	Ground
18	GND	Ground
19	USBDM	USB Data D-
20	USBDP	USB Data D+

Table 2: Pinout of the Expansion Connector X1

3 Power

The PEB-A-002 operates off of a single supply voltage(+3.3V) which is fed through the expansion connector X1 of the PEB. The +3.3V voltage supplies the Ethernet controller which generates a +1.8V core voltage internally. Two LEDs on the PEB-A-002 show the status of the external supply voltage and the core voltage. D3 indicates the presence of the external +3.3V supply voltage and D1 the correct generation of the +1.8V core voltage.

For proper operation the PEB-A-002 must be supplied with a voltage source of 3.3V at the "+3V3" pins on the expansion connector X1.

+3V3: X1 3, 4

Connect both VCC input pins and all GND pins to your power supply.

Corresponding GND: X1 5, 6, 17, 18

Caution:
As a general design rule we recommend connecting all GND pins neighboring signals which are being used in the application circuitry. For maximum EMI performance all GND pins should be connected to a solid ground plane.

4 Ethernet Interface

The Ethernet interface on the PEB-A-002 provides connectivity to the world wide web or a local area network (LAN).

The PEB-A-002 comes populated with an SMSC LAN9500i Ethernet controller supporting 10/100 Mbps Ethernet connectivity. The LAN9500i supports the HP Auto-MDIX function eliminating the need for consideration of a direct connect LAN cable, or a cross-over patch cable. The LAN9500i detects the TX and RX pins of the connected device and automatically configures the PHY TX and RX pins accordingly. The Ethernet controller also features an Auto-negotiation to automatically determine the best speed and duplex mode[1].

Programming of the Ethernet controller is done via USB interface. The USB interface extends from expansion connector X1 to the Ethernet controller (refer to *Table 2* for the pinout).

The Ethernet interface of the PEB-A-002 is accessible at an RJ45 connector (X2). The LEDs for LINK (green) and SPEED (yellow) indication are integrated in the connector (refer to *Figure 4* for the pinout). Additionally LED D2 on the board signals full-duplex mode.

4.1 MAC Address

In a computer network such as a local area network (LAN), the MAC (Media Access Control) address is a ***unique*** computer hardware number. For a connection to the Internet, a table is used to convert the assigned IP number to the hardware's MAC address.

In order to guarantee that the MAC address is unique, all addresses are managed in a central location. PHYTEC has acquired a pool of MAC addresses. The MAC address of the PEB-A-002 is located on the bar code sticker attached to the board. This number is a 12-digit HEX value.

1: a complete list of the Ethernet controllers features can be found in the corresponding Datasheet

5 Technical Specifications

The physical dimensions of the PEB-A-002 are represented in *Figure* 5. The module's profile is ca. 22 mm thick, with a maximum component height of 7 mm on the bottom (connector) side of the PCB and approximately 13 mm on the top (microcontroller) side. The board itself is approximately 1.8 mm thick.

Additional specifications:

• Dimensions:	92 mm x 68 mm
• Weight:	approximately 32 g with all optional components mounted on the circuit board
• Storage temperature:	-40°C to +90°C
• Operating temperature:	standard: 0°C to +70°C extended: -40°C to +85°C
• Humidity:	95 % r.F. not condensed
• Operating voltage:	VCC 3.3 V 5 %, VCC2 5 V 5 %, VBAT 3 V 20 %
• Power consumption:	**TBD**

These specifications describe the standard configuration of the PEB-A-002 as of the printing of this manual.

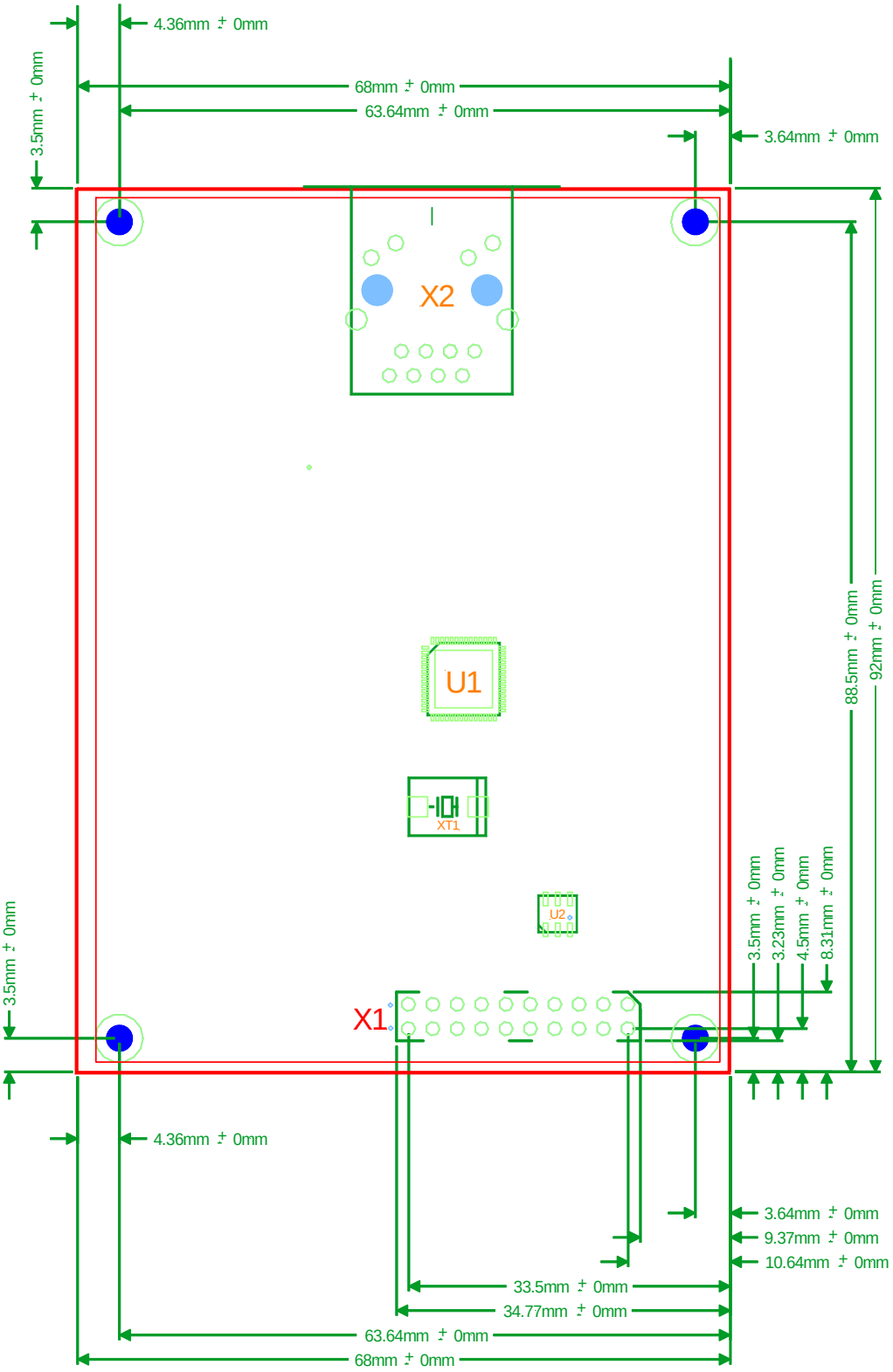


Figure 5: Physical Dimensions

6 Component Placement Diagram

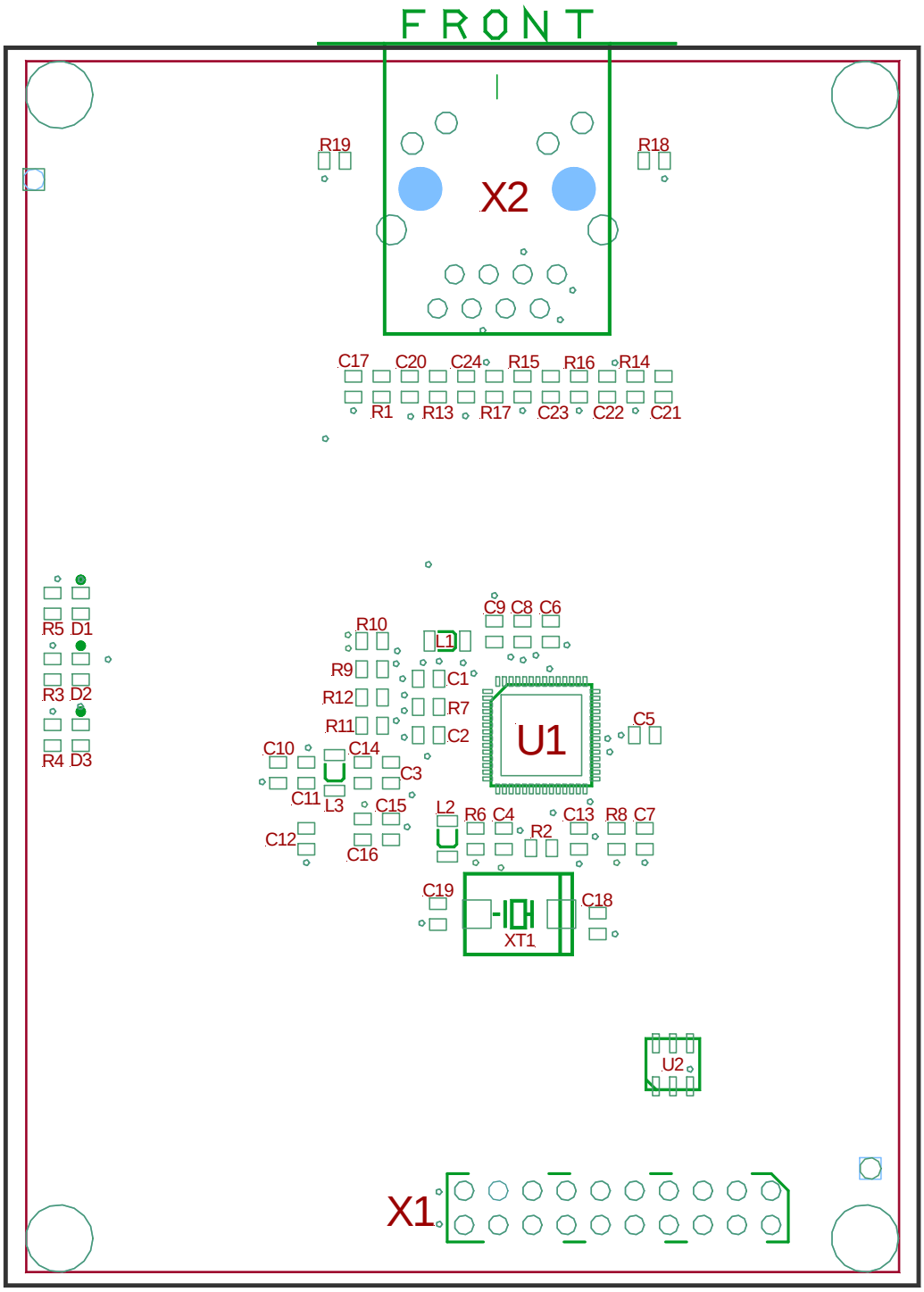


Figure 6: PEB-A-002 Component Placement, Top View

7 Hints for Integrating and Handling the PHYTEC Extension Board

7.1 Integrating the PHYTEC Extension Board

Besides this hardware manual much information is available to facilitate the integration of the PHYTEC Extension Boards into customer applications, or use them as reference design.

1. many answers to common questions can be found at http://www.phytec.de/de/support/faq, or http://www.phytec.eu/europe/support/faql.
2. different support packages are available to support you in all stages of your embedded development. Please visit http://www.phytec.de/de/support/support-pakete.html, or http://www.phytec.eu/europe/support/support-packages.html, or contact our sales team for more details.

7.2 Handling the PHYTEC Extension Board

- **Modifications on the PHYTEC Extension Board**

Removal of various components, such as the Ethernet controller is not advisable given the compact nature of the module. Should this nonetheless be necessary, please ensure that the board as well as surrounding components and sockets remain undamaged while de-soldering. Overheating the board can cause the solder pads to loosen, rendering the module inoperable. Carefully heat neighboring connections in pairs. After a few alternations, components can be removed with the solder-iron tip. Alternatively, a hot air gun can be used to heat and loosen the bonds.

Caution!
If any modifications to the module are performed, regardless of their nature, the manufacturer guarantee is voided.

- **Use of the PHYTEC Extension Board as Reference Design**

Successful use as reference design for custom applications greatly depends on the adherence to the layout design rules for the GND connections. As a general design rule we recommend connecting all GND pins neighboring signals which are being used in the application circuitry. For maximum EMI performance all GND pins should be connected to a solid ground plane. It is also advisable to follow the application information given in the data sheets of the different components.

8 The PEB-A-002 on the on the phyBASE

The phyBASE Carrier Board provides a flexible development platform enabling quick and easy start-up and subsequent programming of the PHYTEC Extension Boards. The Carrier Board design allows easy connection of up to two extension boards featuring various functions that support fast and convenient prototyping and software evaluation. The Carrier Board is compatible with all phyCARDs and PEBs.

The following sections contain specific information relevant to the operation of the PEB-A-002 mounted on the phyBASE Carrier Board.

Note:
Only features of the phyBASE which are needed to support the functioning of the PEB-A-002 are described. Jumper settings and configurations which are not relevant for the use of the PEB-A-002 are not described in the following chapters.

8.1 Overview of the phyBASE Peripherals

The phyBASE is depicted in *Figure 7*. Peripherals required to use the PHYTEC Extension Board PEB-A-002 are highlighted. Additionally all necessary components and peripherals are listed in *Table 3* and *Table 4*. For a more detailed description of each peripheral refer to the appropriate chapter listed in the applicable table.

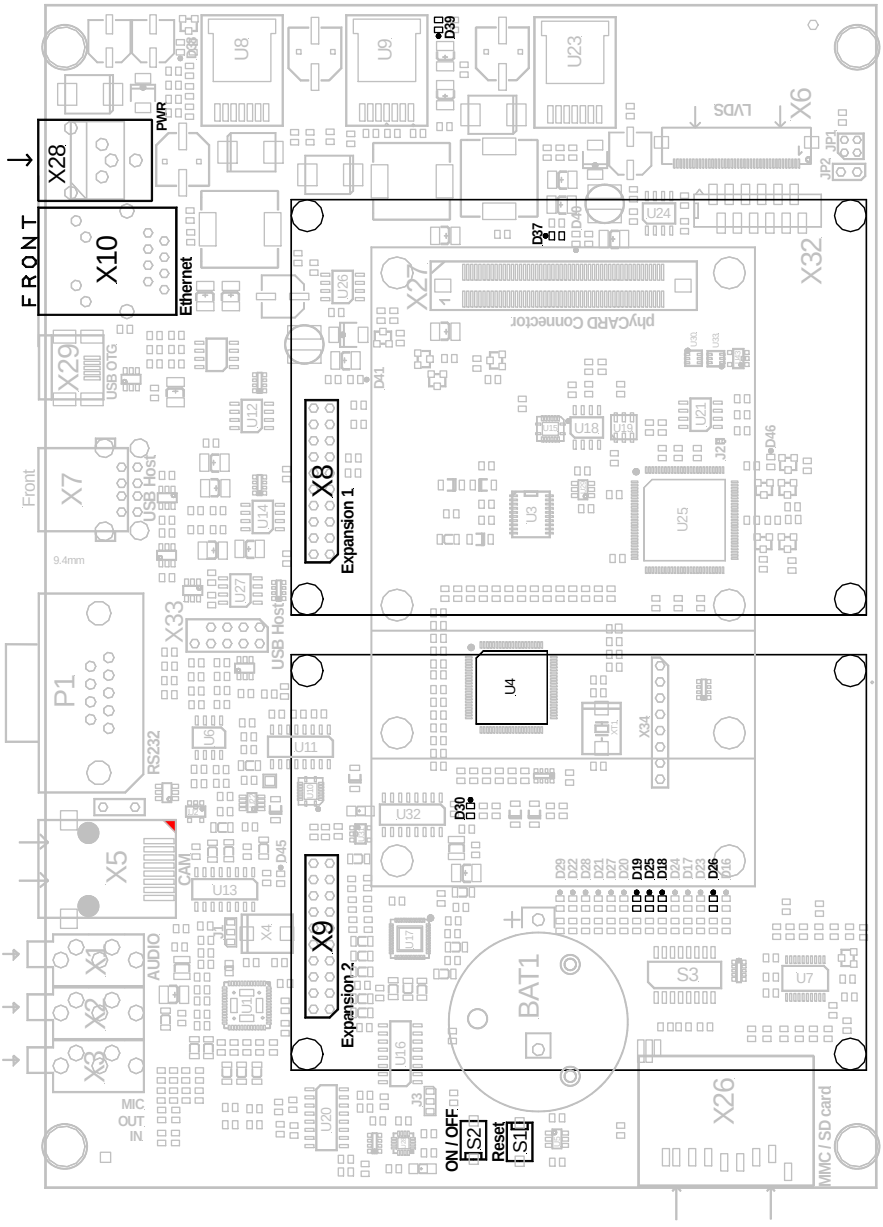


Figure 7: phyBASE Overview of applicable Connectors, LEDs and Buttons

8.1.1 Connectors and Pin Header

Table 3 lists all applicable connectors on the phyBASE. *Figure 7* highlights the location of each connector for easy identification.

Reference Designator	Description	See Section
X8A	Expansion connector 0	*8.2.1*
X9A	Expansion connector 1	*8.2.1*
X28	Wall adapter input power jack to supply main board power (+9 - +36 V)	

Table 3: phyBASE Connectors and Pin Headers related to PEB-A-002

Note:
The signal levels of the I^2C and SPI interface are shifted to VCC3V3 (3.3 V) by level shifters on the phyCARD Carrier Board.

Ensure that all module connections are not to exceed their expressed maximum voltage or current. Maximum signal input values are indicated in the corresponding controller User's Manual/Data Sheets. As damage from improper connections varies according to use and application, it is the user‘s responsibility to take appropriate safety measures to ensure that the module connections are protected from overloading through connected peripherals.

8.1.2 Switches

The phyBASE is populated with some switches which are essential for the operation of the phyCARD module and in further consequence of the PEB on the Carrier Board. *Figure* 7 shows the location of the switches and push buttons.

Button	Description
S1	System Reset Button – system reset signal generation
S2	Power Button – powering on and off main supply voltages of the Carrier Board

Table 4: phyBASE push buttons descriptions

Please refer to the hardware manual of your phyCARD for further information on the functioning of these push buttons.

Additionally a DIP-Switch is available at S3. The configuration of this DIP-Switch does not affect the functioning of the PEB-A-002.

8.1.3 Jumpers

Various jumpers on the phyBASE allow the user flexibility of configuring a limited number of features for development constraint purposes. However none of the jumpers is relevant for the PHYTEC Extension Board's correct functioning.

8.1.4 LEDs

The phyBASE is populated with numerous LEDs to indicate the status of the various USB-Host interfaces, as well as the different supply voltages. Some of them are also important in the use of the PHYTEC Extension Boards. *Figure 7* shows the location of these LEDs. Their function is listed in the table below:

LED	Color	Description	See Section
D18	yellow	USB3 at X8A amber led	*8.2.2.1*
D19	yellow	USB4 at X9A amber led	
D25	green	USB3 at X8A green led	
D26	green	USB4 at X9A green led	
D30	red	USB HUB global led	
D37	green	5V supply voltage for peripherals on the phyBASE	*8.2.1*
D39	green	3V3 supply voltage for peripherals on the phyBASE	

Table 5: phyBASE LEDs descriptions

8.2 Functional Components on the phyBASE Board

This section describes the functional components of the phyBASE Carrier Board supporting the PEB-A-002 Each subsection details a particular connector/interface and associated jumpers for configuring that interface.

8.2.1 Power Supply (X28)

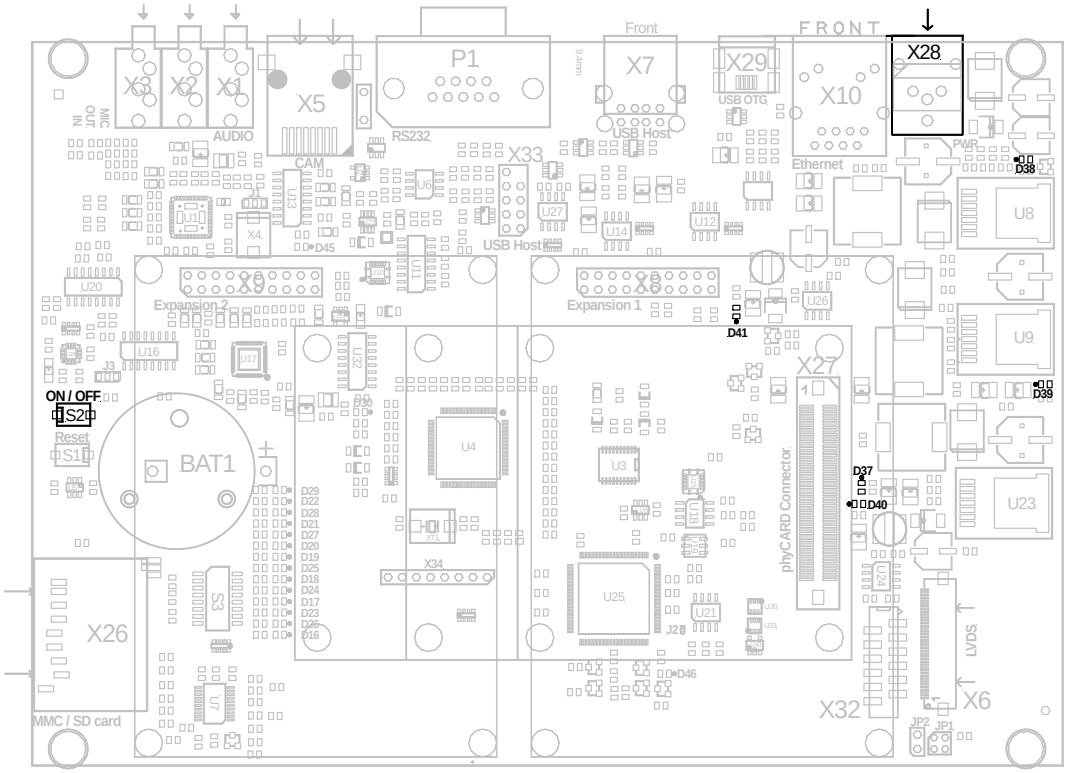


Figure 8: Power adapter

The supply voltage for the PHYTEC Extension Board is derived from the main power supply connected to connector X28 on the phyBASE. It is available at the expansion connectors X8 and X9 as soon, as the phyBASE enters RUN state. The PEBs are powered up only during RUN state of the Carrier Board.

Caution:
Do not use a laboratory adapter to supply power to the Carrier Board! Power spikes during power-on could destroy the phyCARD module and the PEB mounted on the Carrier Board! Do not change modules or jumper settings while the Carrier Board is supplied with power!

Permissible input voltage at X28: +9 - +36 V DC unregulated.

No jumper configuration is required in order to supply power to the PHYTEC Extension Boards!

The phyBASE is assembled with a few power LEDs whose functions are described in the following table:

An LED indicates the availability of the supply voltage at expansion connectors X8 and X9.

LEDs	Color	Description
D39	green	VCC3V3 - 3V3 supply voltage for peripherals on the phyBASE

Table 6: Power state LEDs

Please refer to the corresponding section in your phyCARD Hardware Manual for detailed information on suitable power supplies and the different power states.

8.2.2 Expansion connectors (X8A, X9A)

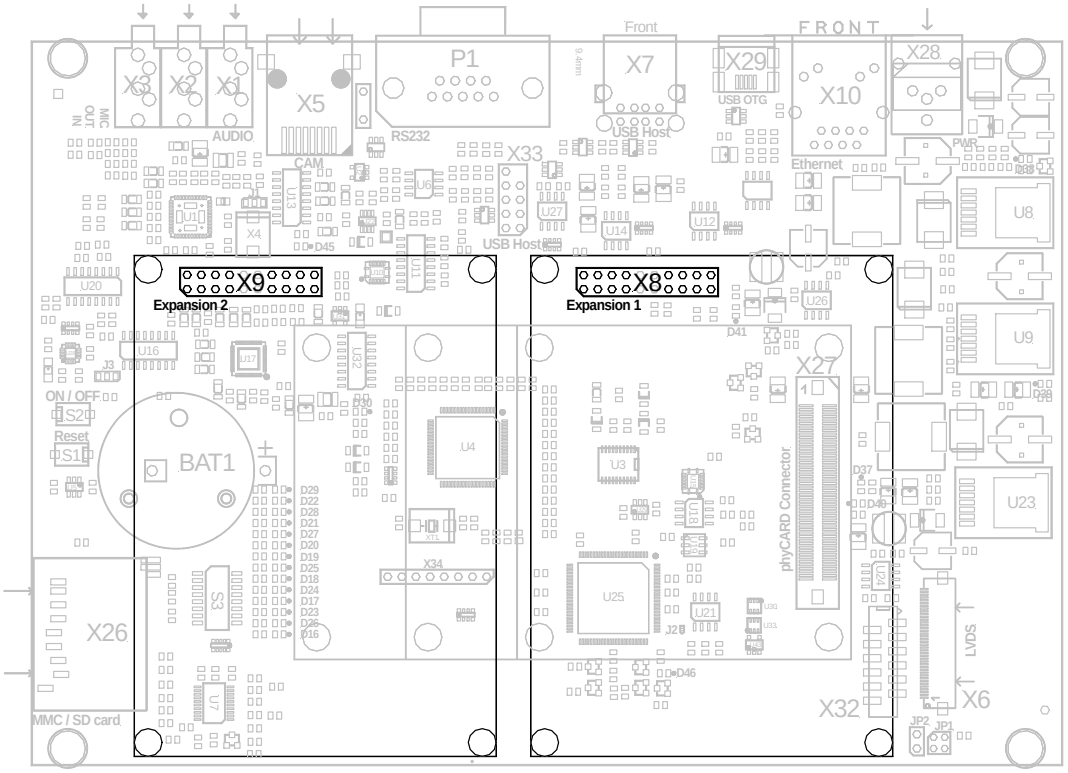


Figure 9: Expansion connector X8A, X9A

The expansion connectors X8A and X9A provide an easy way to connect the PHYTEC Extension Boards to the phyBASE and therefore add other functions and features to it.

As can be seen in *Figure 9* the location of the connectors allows to expand the functionality without expanding the physical dimensions. Mounting wholes can be used to screw the PEBs to the phyBASE.

Various standard interfaces such as USB, SPI and I^2C as well as different supply voltages and one GPIO are available at the pin header rows. Usage of the PEB-A-002 requires the USB interface and a +3.3V supply voltage. A pinout showing all signals which extend on the PEB-A-002 is shown in *Table 2*.

8.2.2.1 USB Host Connectivity

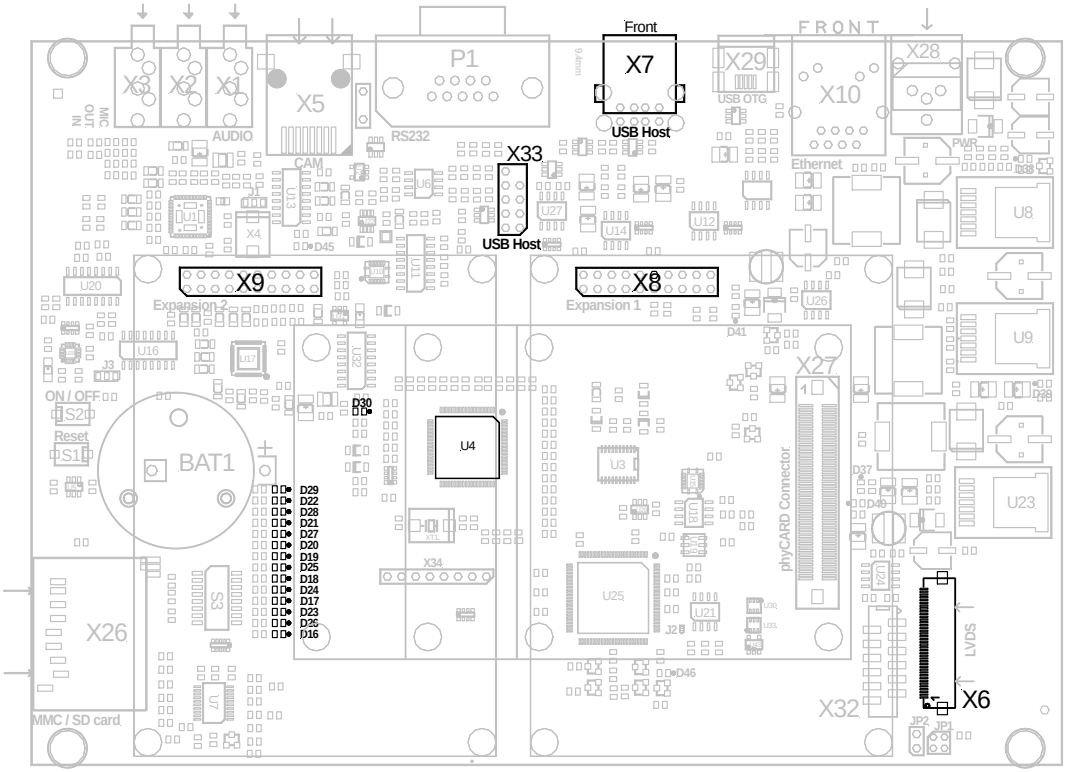


Figure 10: Components supporting the USB host interface

Programming of the Ethernet controller on the PEB-A-002 is done via the phyCARD's USB interface.

A USB hub controller at U4 on the Carrier Board expands the phyCARD's USB interface into seven downstream facing ports. Two of them (USB3 and USB4) are available at expansion connectors X8A and X9A. The USB interface extends from the expansion connector X1 on the PEB-A-002 to the Ethernet controller (refer to *Table 2* for the pinout).

Note:
The USB interfaces at expansion connectors X8A and X9A provide only the data lines D+ and D-. They do not feature a supply line Vbus.

Table 7 shows the distribution of the two downstream facing ports to the expansion connectors.

USB hub port #	Connector	Connector Type
USB3	X8A	20 pin header row (pins 19 (D-) and 20 (D+))
USB4	X9A	20 pin header row (pins 19 (D-) and 20 (D+))

Table 7: Distribution of the USB hub's (U4) ports

LEDs D18, D19 and D25, D26 as well as LED D30 signal correct functioning and use of the USB host interfaces. Table 8 shows the assignment of the LEDs to the different USB ports.

LED	Color	Description
D18	yellow	USB3 at X8A amber led
D19	yellow	USB4 at X9A amber led
D25	green	USB3 at X8A green led
D26	green	USB4 at X9A green led
D30	red	USB HUB global led

Table 8: phyBASE LEDs descriptions

Appropriate drivers are available within the BSPs[1] provided with the different phyCARD SBCs (*please refer to the Quickstart Manual "OSELAS.BSP()"*).

[1]: To ensure that the PEB of your choice is supported by the BSP use only the latest BSP, or check on the PHYTEC website from which version on the driver is implemented.

8.2.3 Ethernet Connectivity (X10)

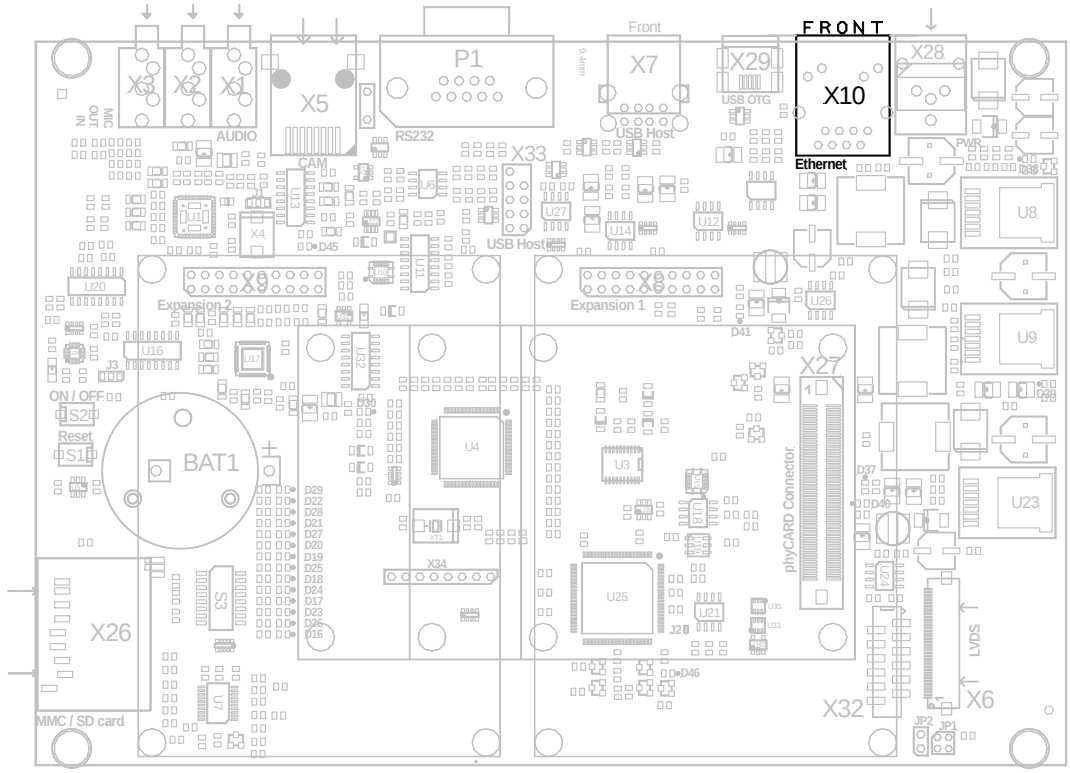


Figure 11: Ethernet interface at connector X10

The Ethernet interface of the phyCARD is accessible at an RJ45 connector (X10) on the Carrier Board.

It is independent of the Ethernet interface on the PEB-A-002. Both can be used at the same time.

9 Revision History

Date	Version numbers	Changes in this manual
20-Apr-2011	Manual L-756e_1	First edition

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